



Title of Change:	RSL10: Datasheet update.	
Effective date:	1 June 2018	
Contact information:	Contact your local ON Semiconductor Sales Office, or <Christophe.waelchli@onsemi.com>	
Type of notification:	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.	
Change category:	<input type="checkbox"/> Wafer Fab Change <input type="checkbox"/> Assembly Change <input type="checkbox"/> Test Change <input checked="" type="checkbox"/> Other: <u>Datasheet update</u>	
Change Sub-Category(s):	<input type="checkbox"/> Manufacturing Site Change/Addition <input type="checkbox"/> Material Change <input checked="" type="checkbox"/> Datasheet/Product Doc change <input type="checkbox"/> Manufacturing Process Change <input type="checkbox"/> Product specific change <input type="checkbox"/> Shipping/Packaging/Marking <input type="checkbox"/> Other: Qualification update	
Sites Affected:	ON Semiconductor Sites: None	External Foundry/Subcon Sites: None

Description and Purpose:

This Product Bulletin indicates that a new version of the datasheet for the RSL10 Radio IC is released.

The new version of the RSL10 datasheet is Rev3. The old version was the Rev2 version.

Two changes were made:**1) Table 2. RECOMMENDED OPERATING CONDITIONS, Supply voltage operating range, page 3:**

Vbat Min changed from 1.25V to 1.18

The conditions that are required to reach 1.1 V are changing from:

"In order to be able to use a VBAT Min of 1.1 V:

- The minimum VDDM target trimming voltage of 1.05 V should be used.
- VBAT should be connected to VCC at the schematic level

In this configuration, VBAT shouldn't exceed 1.32V. Please contact your ON sales representatives in case of questions."

To:

"In order to be able to use a VBAT Min of 1.1 V:

- Maximum Tx power 0 dBm.
- SYSCLK <= 24 MHz.
- Functional temperature range limited to 0-50 deg C

The following trimming parameters should be used:

- VCC = 1.10 V
- VDDC = 0.92 V
- VDDM = 1.05 V, will be limited by VCC at end of battery life
- VDDRF = 1.05 V, will be limited by VCC at end of battery life. VDDPA should be disabled



RSL10 should enter in end-of-battery-life operating mode if VCC falls below 1.03 V. VCC will remain above 1.03 V if VBAT >= 1.10 V under the restricted operating conditions described above."

2) Table 9. CHIP INTERFACE SPECIFICATIONS

NRESET (Reset pin): We are adding a note that mentions that the Pull up for NRESET is fixed at 200Kohm and is not selectable. We are adding the following note: "All DIOs can be configured to no pull."

List of Affected Standard Parts:

NCH-RSL10-101WC51-ABG
NCH-RSL10-101Q48-ABG

Please be informed that there may be Customer Specific parts impacted by this Notification, thus MPN & CPN info will not be reflected in the parts list of this Generic document. Instead please click the link to the addendum PCN copy provided in the email notification to see full list of affected products specific to your company.



Appendix A: Changed Products

Product	Customer Part Number
NCH-RSL10-101Q48-ABG	
NCH-RSL10-101WC51-ABG	